



SPECIFICATION

• Supplier : Samsung electro-mechanics • Samsung P/N : CL21B155KAFNNNE

Product : Multi-layer Ceramic Capacitor
 Description : CAP, 1.5μF, 25V, ±10%, X7R, 0805

A. Samsung Part Number

<u>CL</u> <u>21</u> <u>B</u> <u>155</u> <u>K</u> <u>A</u> <u>F</u> <u>N</u> <u>N</u> <u>N</u> <u>E</u> ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩ ⑪

1	Series	Samsung Multi-layer Ceramic Capacitor								
2	Size	0805 (inch c	ode)	L: 2.0	± 0.1	mm	W:	1.25	± 0.1	mm
3	Dielectric	X7R		8	Inner electrode			Ni		
4	Capacitance	1.5 μF			Termin	ation		Cu		
(5)	Capacitance	±10 %			Plating			Sn 100	0%	(Pb Free)
	tolerance			9	Produc	t		Norma	al	
6	Rated Voltage	25 V		10	Special			Reserved for future use		
7	Thickness	1.25 ± 0.1	mm	11)	Packag	ing		Embos	ssed T	ype, 7" reel(2,000ea)

B. Samsung Reliability Test and Judgement condition

	Performance	Test condition						
Capacitance	Within specified tolerance	1kHz±10% 1.0±0.2Vrms						
Tan δ (DF)	0.1 max.							
Insulation More than 100Mohm⋅μF		Rated Voltage 60~120 sec.						
Resistance								
Appearance	No abnormal exterior appearance	Visual inspection						
Withstanding	No dielectric breakdown or	250% of the rated voltage						
Voltage	mechanical breakdown							
Temperature	X7R							
Characteristics	(From -55℃ to 125℃, Capacitance change should be within ±15%)							
Adhesive Strength	No peeling shall be occur on the	500g·F, for 10±1 sec.						
of Termination	terminal electrode							
Bending Strength	Capacitance change: within ±12.5%	Bending to the limit (1mm)						
		with 1.0mm/sec.						
Solderability	More than 75% of terminal surface	SnAg3.0Cu0.5 solder						
	is to be soldered newly	245±5℃, 3±0.3sec.						
		(preheating : 80~120 ℃ for 10~30sec.)						
Resistance to	Capacitance change: within ±7.5%	Solder pot : 270±5℃, 10±1sec.						
Soldering heat	Tan δ, IR : initial spec.							

	Performance		Test condition				
Vibration Test	Capacitance change: within ±5%		Amplitude : 1.5mm				
	Tan δ, IR : initial spec.		From 10Hz to 55Hz (return : 1min.)				
			2hours × 3 direction (x, y, z)				
Moisture	Capacitance change: withir	า ±12.5%	With rated voltage				
Resistance	Tan δ: 0.125 max		40±2℃, 90~95%RH, 500+12/-0 hour				
	IR : More than 12.5MΩ·μF						
High Temperature	Capacitance change : withir	า ±12.5%	With 150% of the rated voltage				
Resistance	Tan δ: 0.125 max		Max. operating temperature				
	IR : More than 25ΜΩ·μΓ						
			1000+48/-0 hour				
Temperature	Capacitance change: withir	า ±7.5%	1 cycle condition				
Cycling	Tan δ, IR : initial spec.		Min. operating temperature \rightarrow 25 $^{\circ}$ C				
			$ ightarrow$ Max. operating temperature $ ightarrow$ 25 $^{\circ}$ C				
			5 cycles test				

C. Recommended Soldering method :

Reflow (Reflow Peak Temperature : 260+0/-5 $^{\circ}$ C, 10sec. Max)

^{*} For the more detail Specification, Please refer to the Samsung MLCC catalogue.